

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Appln. Of: HONDA

Serial No.: 10/763,554

Filed: January 23, 2004

For: WARP-SUPPRESSED SEMICONDUCTOR DEVICE

Group: 2826 Confirmation No. 7561

Examiner: Williams, Alexander O. DOCKET: NEC 26485

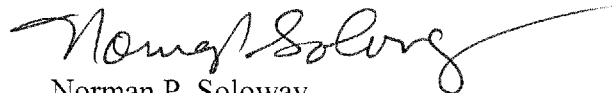
MAIL STOP ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**APPLICANTS' INTERVIEW SUMMARY**

Dear Sir:

The Interview Summary attached to the Supplemental Notice of Allowability and Notice of Allowance mailed April 28, 2010 accurately reflects the substance of the April 21, 2010 interview.

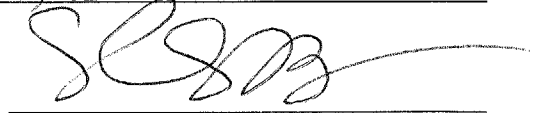
Respectfully submitted,



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**CERTIFICATE OF ELECTRONIC FILING**

I hereby certify that this paper is being deposited with the United States Patent Office via the electronic filing procedure on April 28, 2010 at Tucson, Arizona.



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